

Thermoelectricity of silicon nanostructures

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Nanotechnology pilot plant

- ♦ **Nanostructuring:** lithography, thin film deposition, device prototyping
- ♦ **Nanoanalysis:** electron microscopy, optical spectroscopy, positron annihilation
- ♦ 1800 m² labs, 620 m² cleanroom

Research

- ♦ **Energy conversion:** photovoltaics, photonics
- ♦ **Energy storage:** batteries
- ♦ **Energy recycling:** thermoelectrics

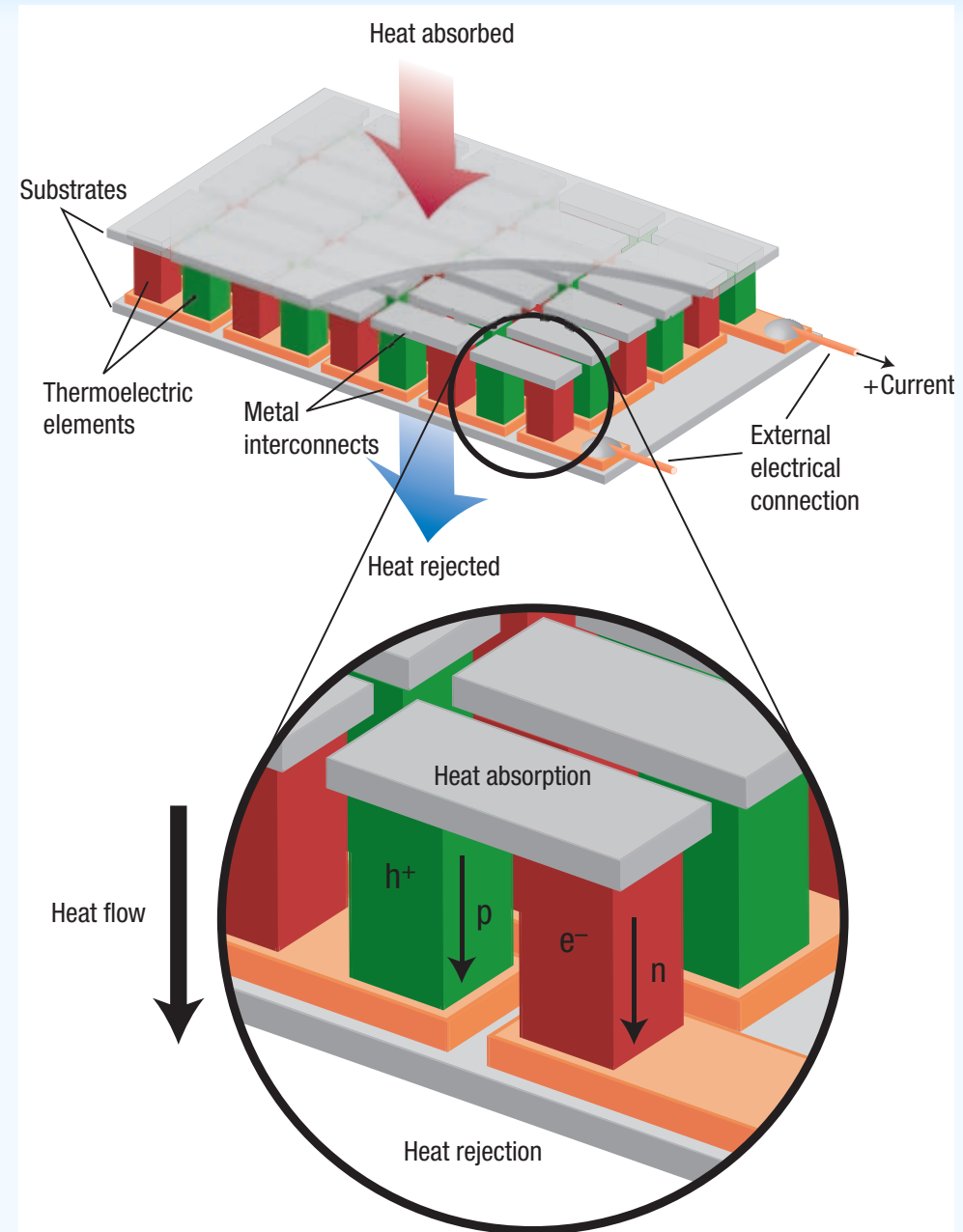
Thermoelectric devices

Thermoelectric generator

- ◆ Heat → electricity
- ◆ Heat flow drives free e^- and h^+ from hot to cold

Seebeck effect

$$U = \int_{T_1}^{T_2} S dT$$



[Snyder, Toberer 2008]

Thermoelectrics

ANNALEN DER PHYSIK.

JAHRGANG 1826, ERSTES STÜCK.

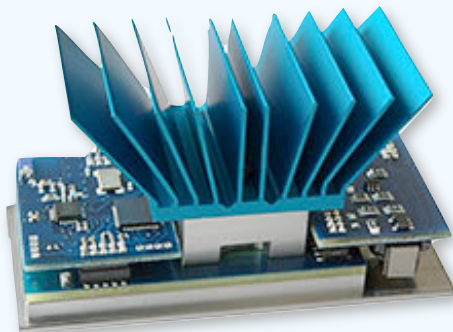
I.

*Ueber die magnetische Polarisation der Metalle und
Erze durch Temperatur-Differenz;*

VON

Dr. T. J. SEEBECK.

Thomas J. Seebeck
(1770–1831)



Power Node
[Micropelt]



[NASA]

Basic terms of thermoelectrics

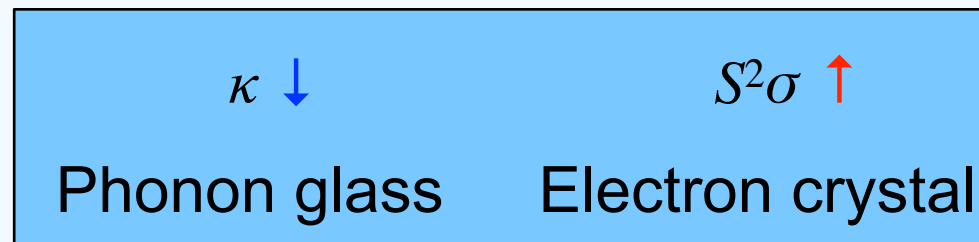
Conversion efficiency

- ◆ Determined by the dimensionless **figure of merit**

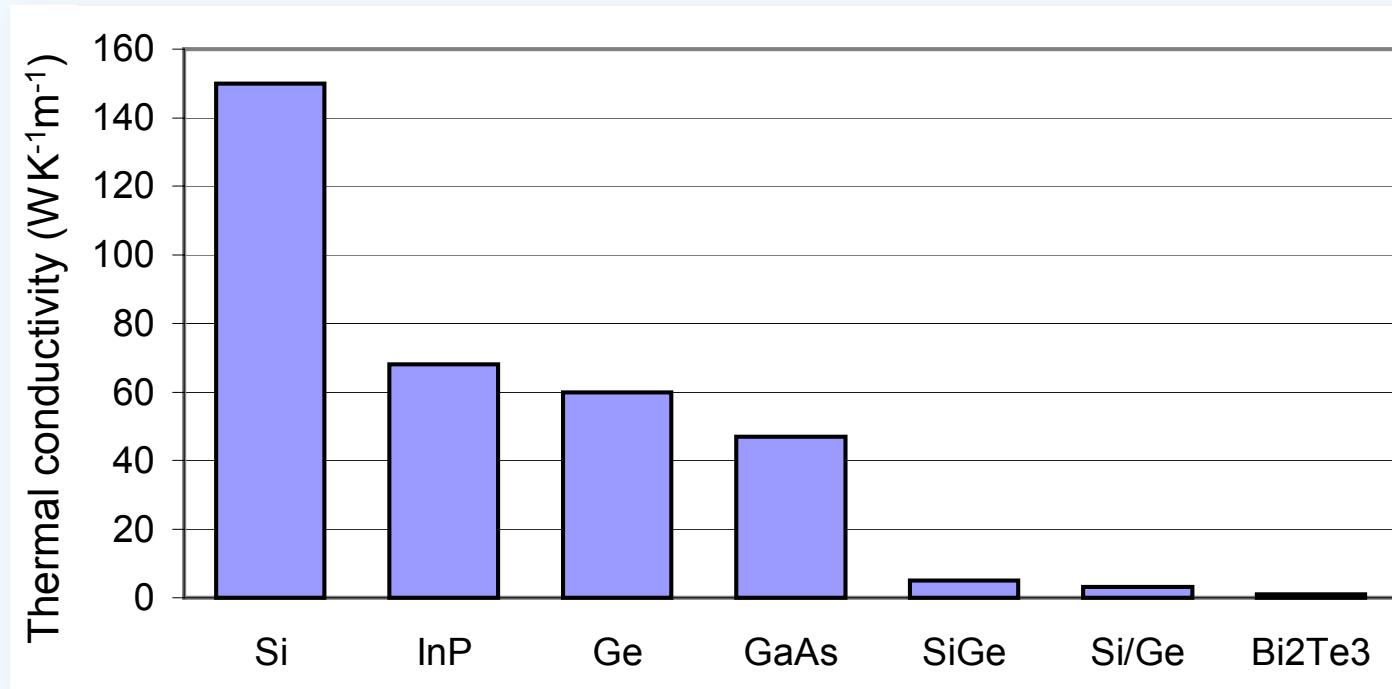
$$ZT = \frac{S^2 \sigma}{\kappa} T$$

($S^2 \sigma$ **power factor**, σ electrical conductivity, κ thermal conductivity, T temperature)

- ◆ High ZT requires $S^2 \sigma \uparrow$ and $\kappa = \kappa_e + \kappa_{ph} \downarrow$
- ◆ Problem: Coupling between electrical and thermal conductivity
- ◆ Topical materials $ZT \leq 1$
- ◆ Tailor materials by nanostructuring: Superlattices, nanowires, quantum dots



Si as thermoelectric material

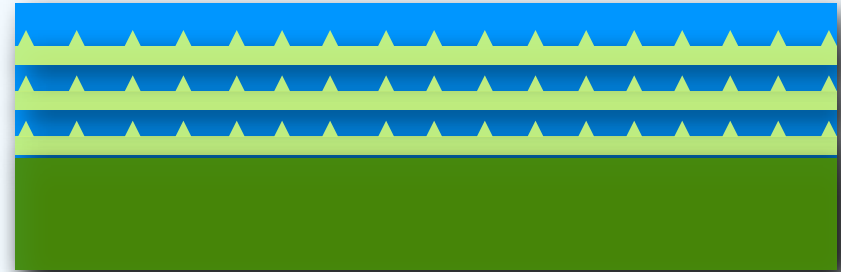


Thermal conductivity of the lattice κ_{ph} for semiconductor single crystals
[Fan 2002]

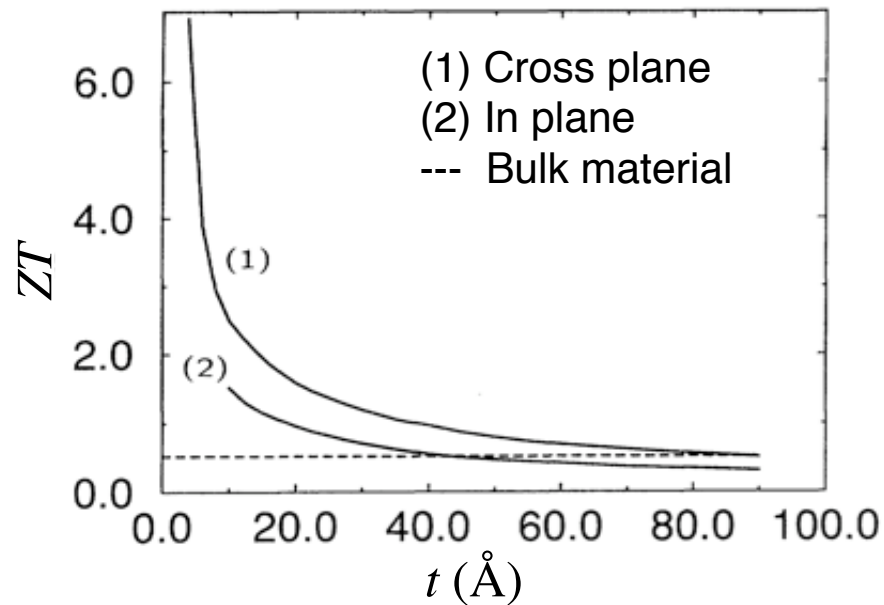
Nanostructuring

- ◆ Bandstructure engineering
- ◆ Phonon scattering at interfaces, porous surfaces, defects

Superlattices



Single-crystalline multilayer and quantum dot superlattices



$$ZT = \frac{S^2 \sigma}{\kappa_{\text{ph}} + \kappa_{\text{e}}} T$$

Thermoelectric efficiency ZT for anisotropic Bi_2Te_3 layers of the thickness t
[Hicks, Dresselhaus 1993]

Reduction of thermal conductivity

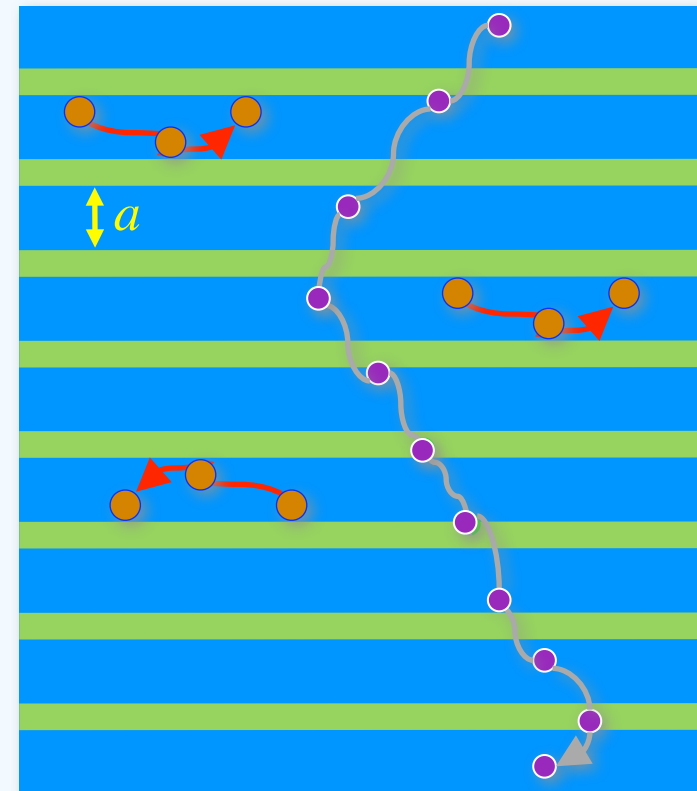
Cross-plane transport in SL $\rightarrow$ Coherent phonon scattering at interfaces

	Mean free path
Phonon ●	$\ell \approx 260 \text{ nm}$
Electron ●	$\ell_e \sim 10 \text{ nm}$

$$\kappa_{\text{ph}} = \frac{1}{3} C v \ell$$

(C lattice heat capacity, v speed of sound)

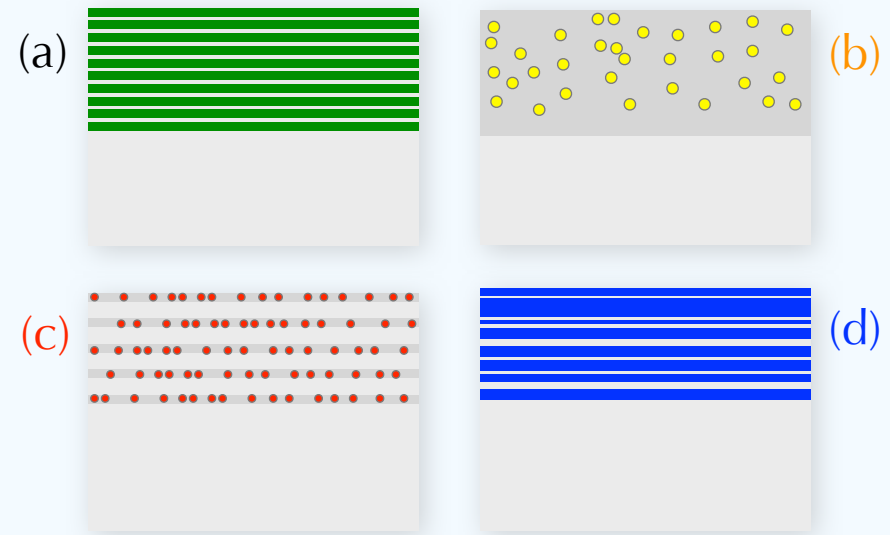
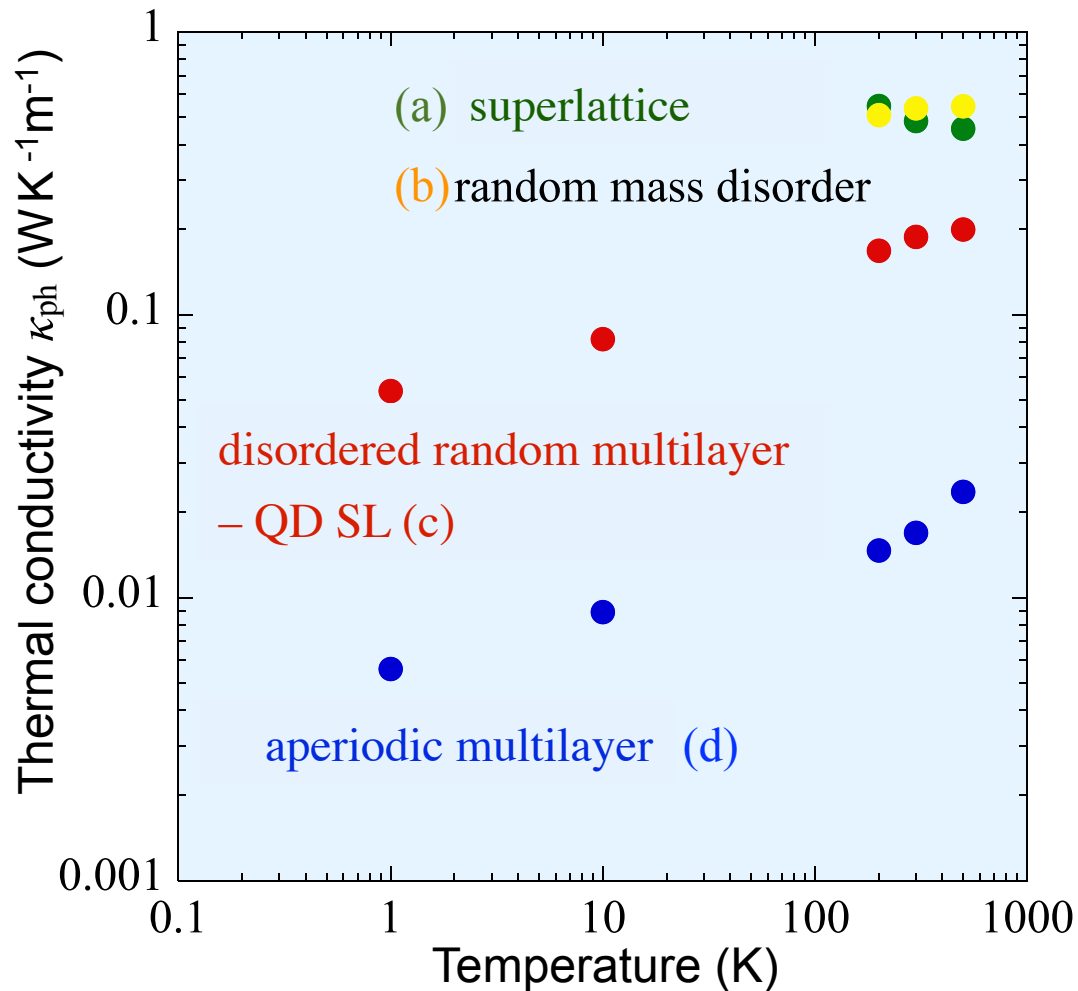
If layer thickness $a < \ell$, the thermal conductivity of the lattice κ_{ph} is reduced.



Phonon scattering at interfaces

Phonon scattering

Superlattices, composites, quantum dot SLs, random multilayers



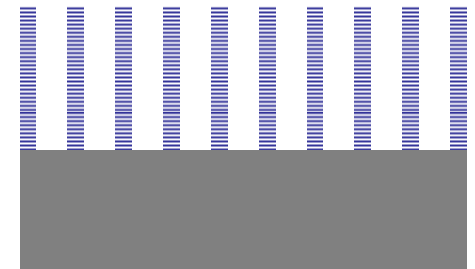
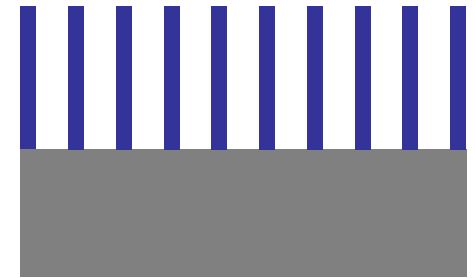
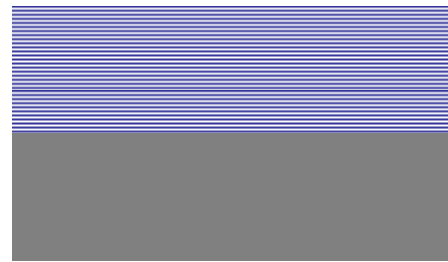
Anderson localization of phonons
in **nonperiodic structures**

MD Simulation

[Frachioni, White 2012]

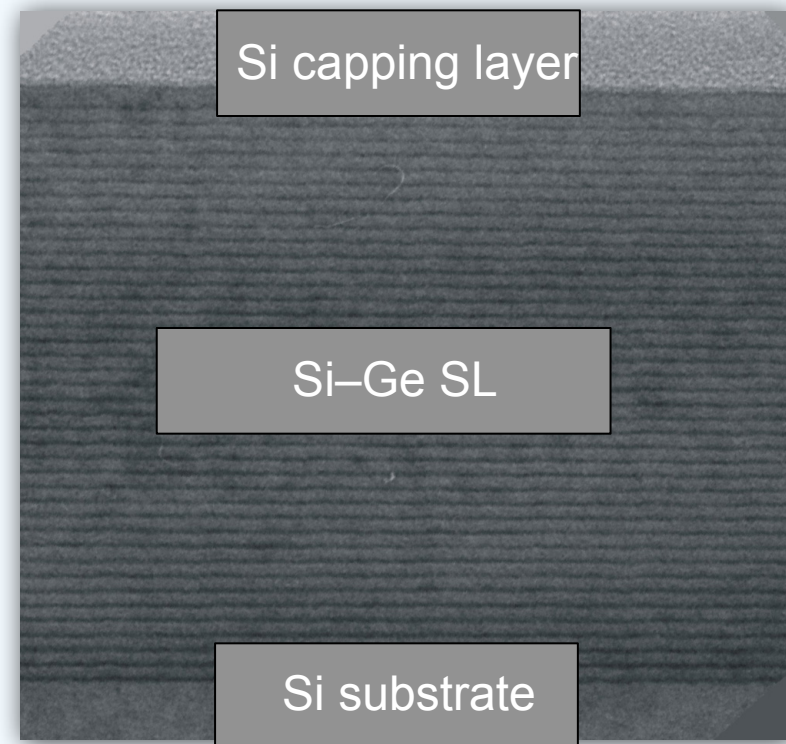
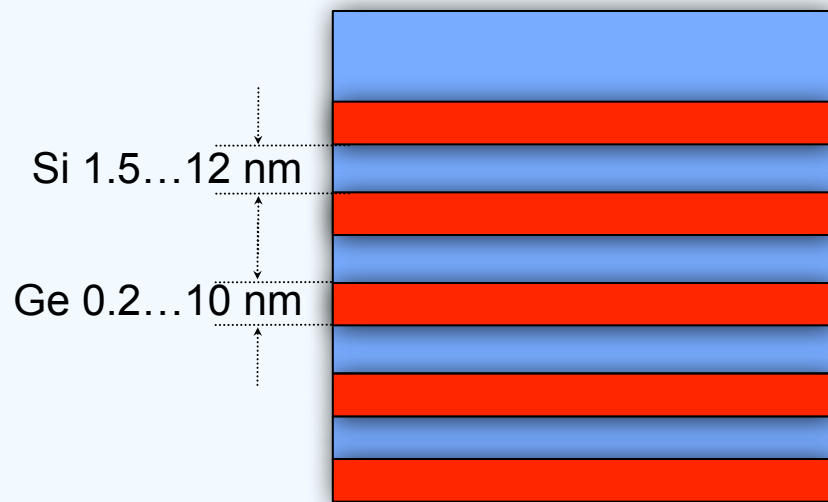
Reduction in the thermal conductivity

- Different approaches through nanostructures like
 - Superlattices (SL)
 - Nanowires (NW)
 - Quantum-dot superlattices (QDSL)
 - Nanowires containing superlattices (SLNW)



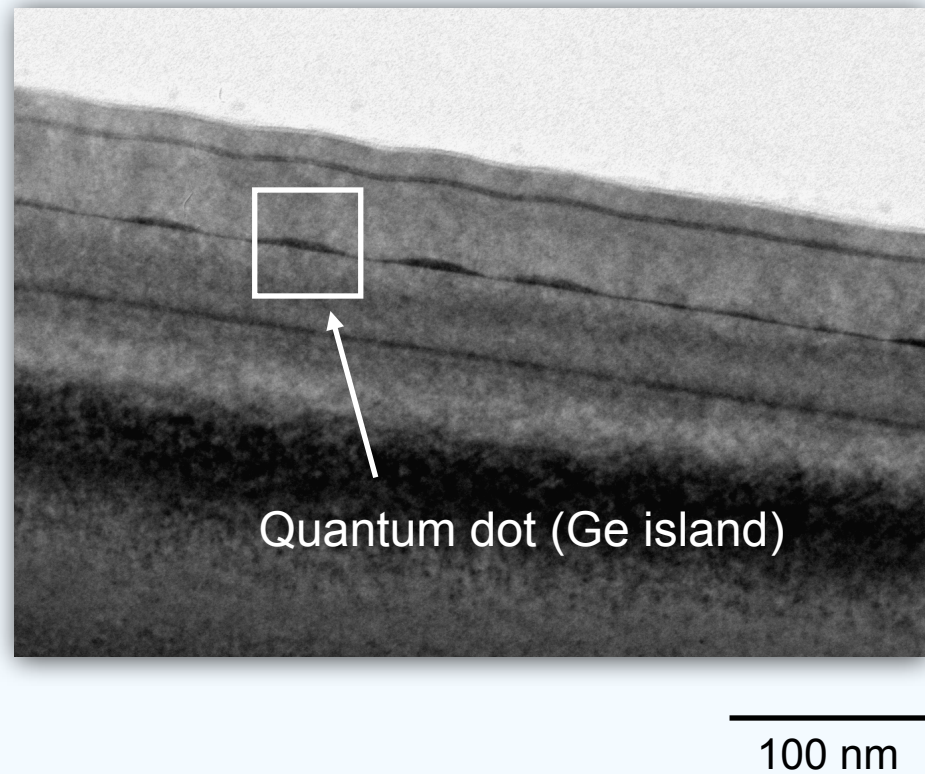
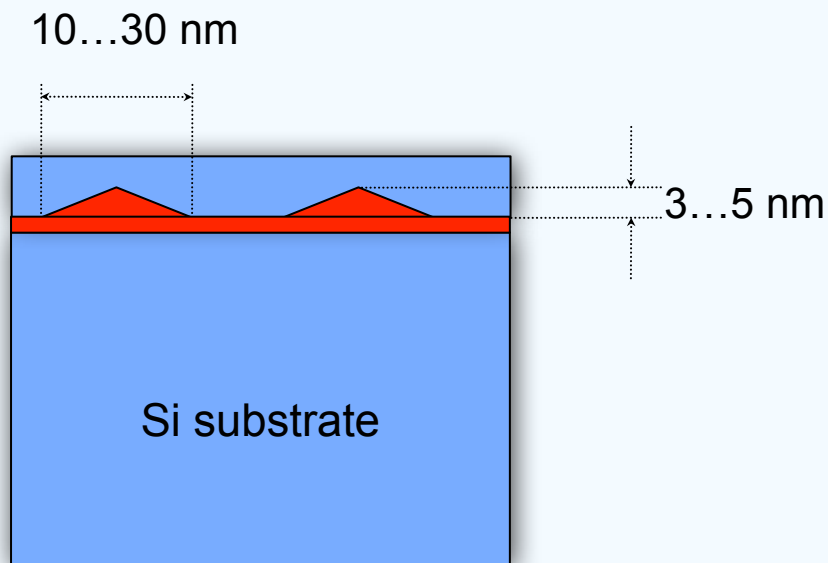
MBE of Si-Ge layers

- ♦ Stack of alternating layers of Si and $\text{Si}_{1-x}\text{Ge}_x$ alloy
- ♦ Precision of single layers: ± 0.2 nm



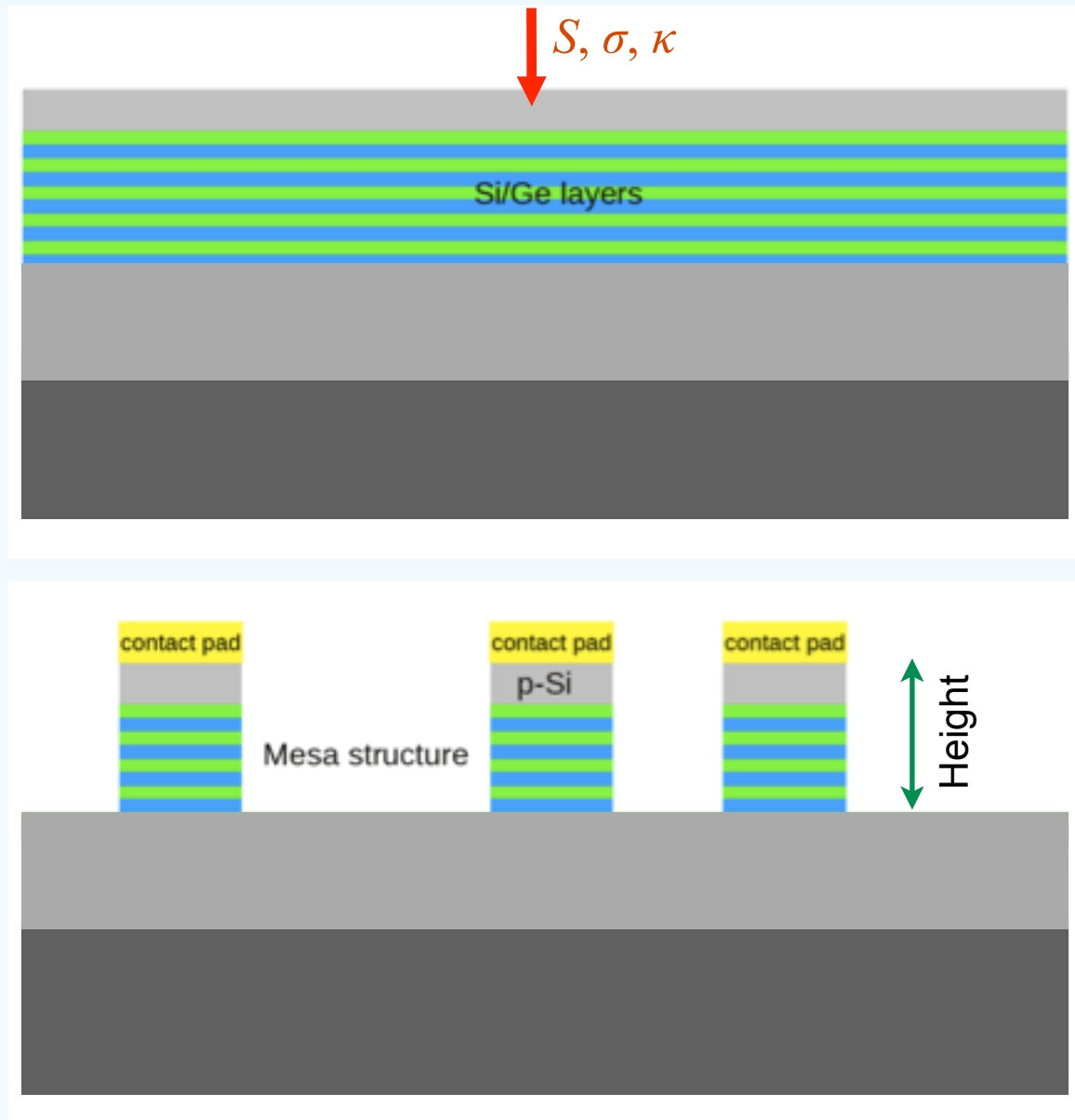
Quantum dot Si-Ge superlattice

- ♦ (001), (111) orientation of the Si substrate
- ♦ Si (111) → flat layers
- ♦ Si (100) → Ge islands (density $\sim 10^9 \dots 10^{11} \text{ cm}^{-2}$)

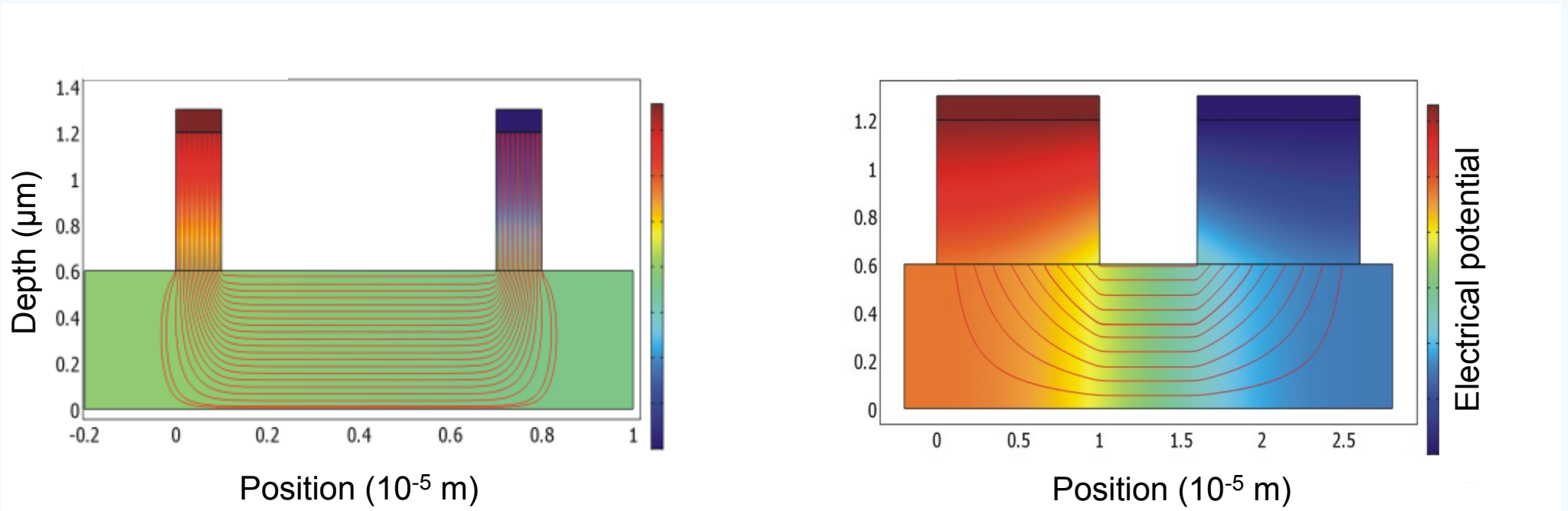


Cross-plane measurement

- ◆ Direct measurement of thin layers on a substrate rather demanding
- ◆ Cross-plane and in-plane electrical conductivity of specially designed samples (mesa) obtained via transmission line model
- ◆ Determination of the contact resistances
- ◆ Small error of resistivity measurement only with slight doping



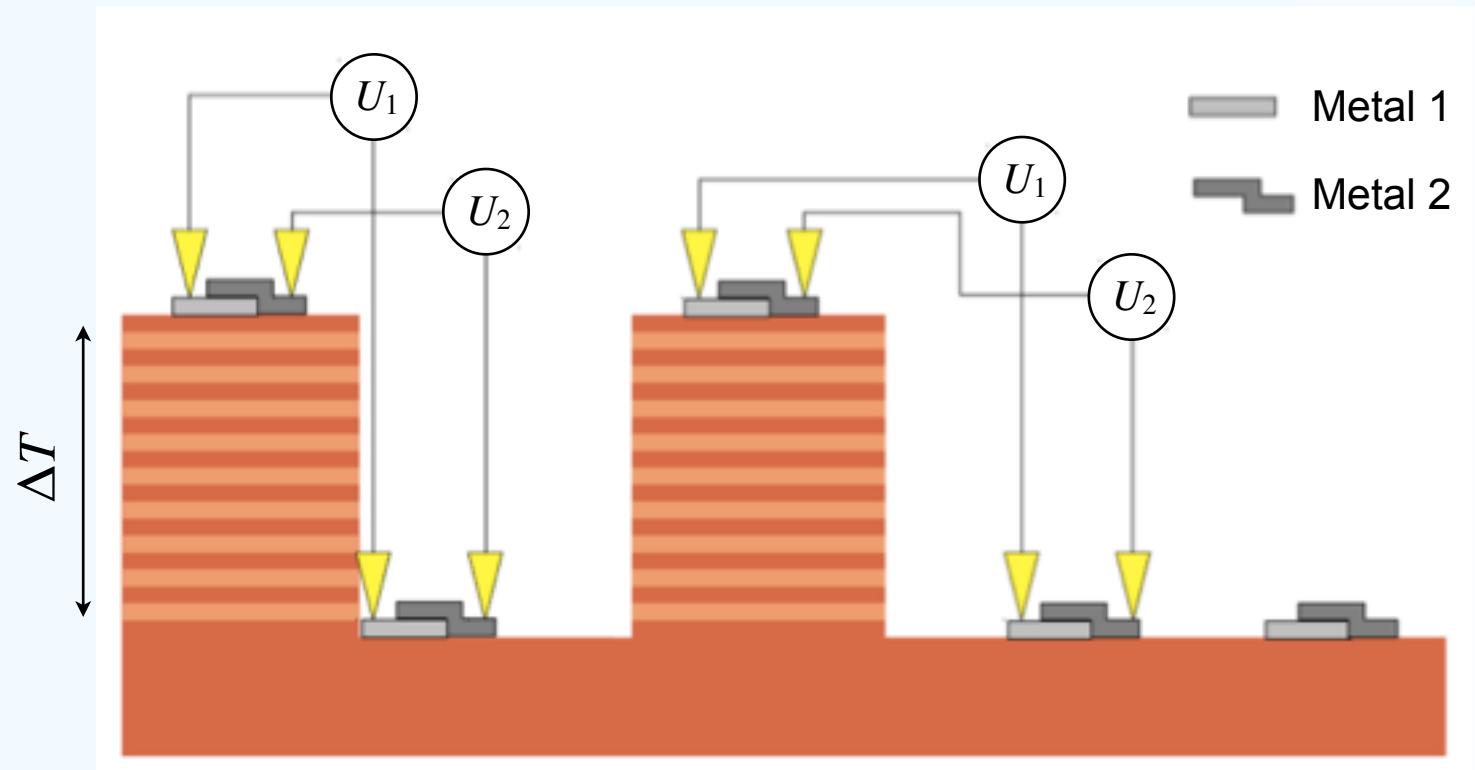
Current-voltage distribution



Finite-element simulation of the current-voltage distribution
in mesa structures of different widths

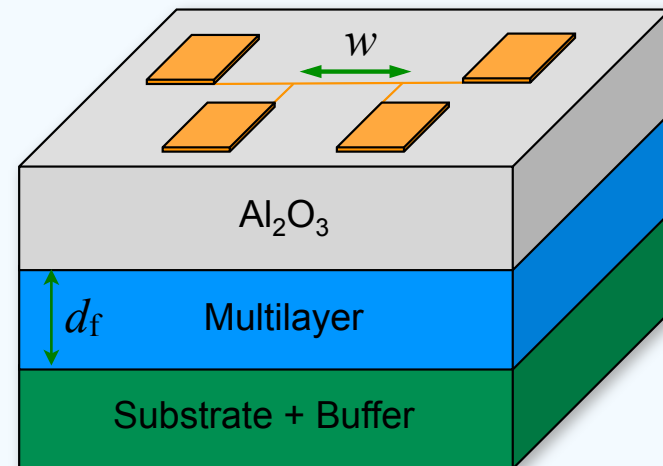
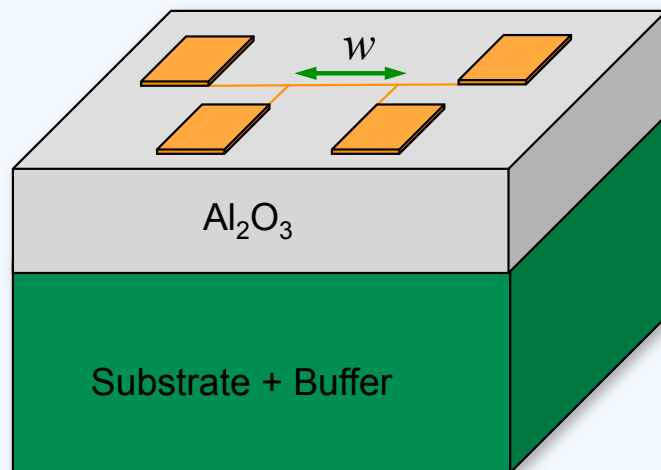
Seebeck cross-plane measurement

- ♦ Measurement of the thermoelectric voltage close to the superlattice mesa
- ♦ Metals 1 and 2 form the thermocouple
 - determination of the temperature difference ΔT
- ♦ Measurement of further thermovoltages to eliminate the in-plane contribution
- ♦ Where to place the heater?



3ω measurements

- ◆ Deposition of a 100 nm insulating Al_2O_3 layer by ALD
- ◆ Reference sample without the multilayer structure
- ◆ Differential 3ω measurement of the thermal conductivity of thin films, $U_{3\omega} = f(\kappa)$



Temperature increase

- ◆ Current $I = I_0 \cos \omega t$ is related to the increase in temperature

$$\Delta T = \Delta T_0 \cos(2\omega t + \varphi)$$

- ◆ Resistance of a metallic wire

$$R = R_0(1 + \alpha \Delta T)$$

$$\text{Voltage } U = RI = R_0[1 + \alpha \Delta T_0 \cos(2\omega t + \varphi)] I_0 \cos \omega t$$

- ◆ The $U_{3\omega} = 1/2 I_0 R_0 \alpha \Delta T_0$ component contains information about the thermal properties of the underlying matter and is measured with a lock-in amplifier.

[Jacquot *et al.* ETC 1999]

Thermal conductivity of the thin film

$$\kappa = \frac{P(t_{\text{tot}} - t_{\text{ref}})}{2wb(\text{Re}(\Delta T_{\text{tot}}) - \text{Re}(\Delta T_{\text{ref}}))}$$

Measurement at a reference sample of the thickness t_{ref} and the sample containing the thin film with the total thickness t_{tot} (b width of the bolometer)

Thin film thermal conductivity

1D heat flow

Measurement with one bolometer stripe, width $b \gg d_f$

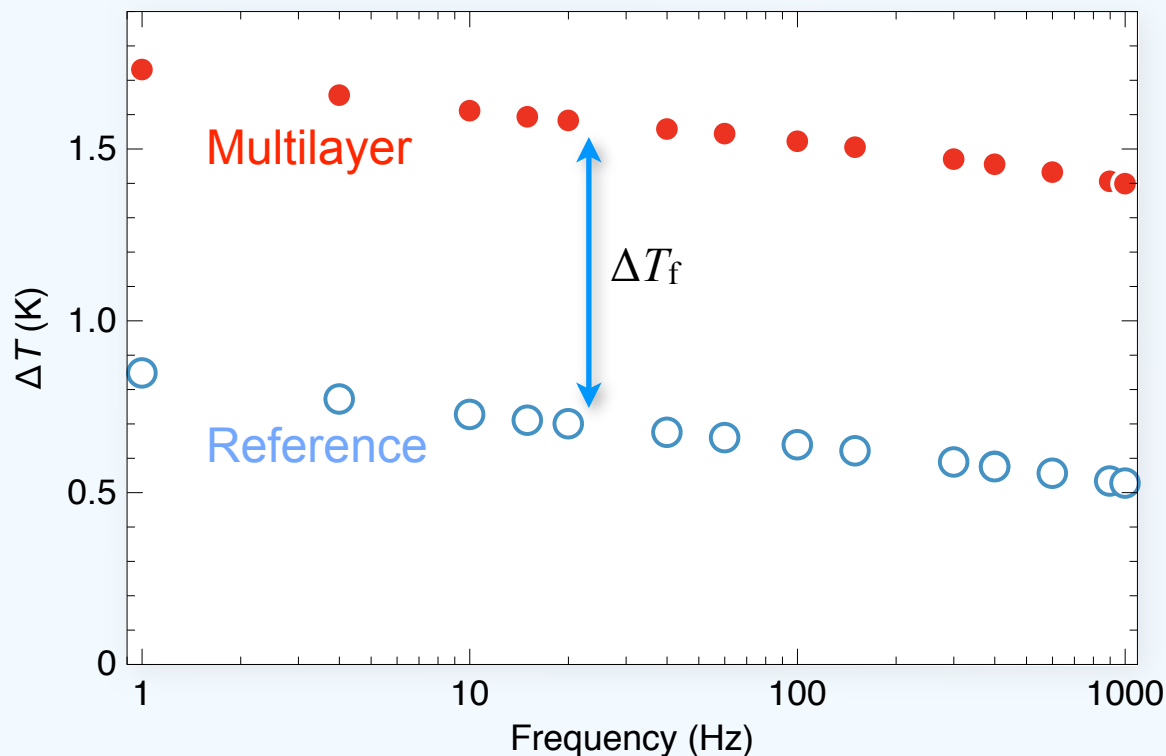
$\Delta T_f \rightarrow$ 1D thermal conductivity κ_{1D}

2D heat flow

Measurements with two bolometer stripes, b_1 and b_2

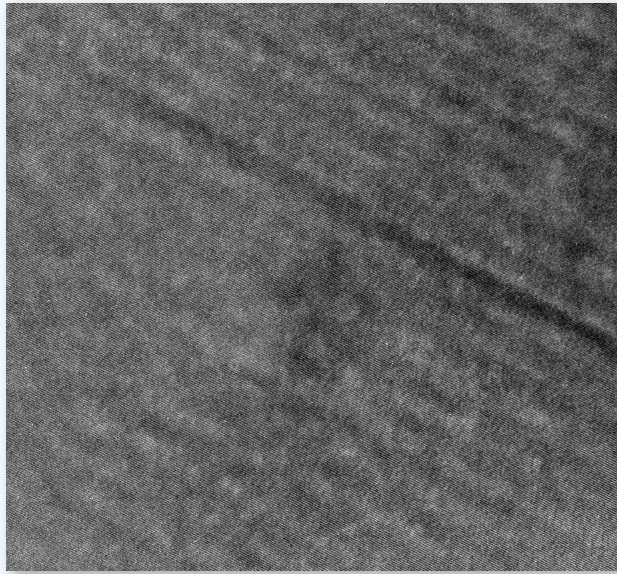
$\Delta T_f \rightarrow$ in-plane thermal conductivity $\kappa_{\parallel}$

$\rightarrow$ cross-plane thermal conductivity $\kappa_{\perp}$



Bolometric temperature increase ΔT measured in a multilayer and a reference sample as a function of the frequency

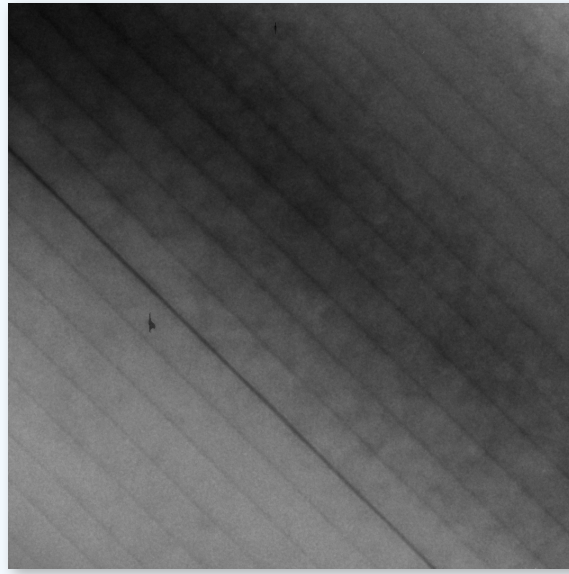
SiGe Superlattices



10 nm

0.2 nm Ge + 3.3 nm Si

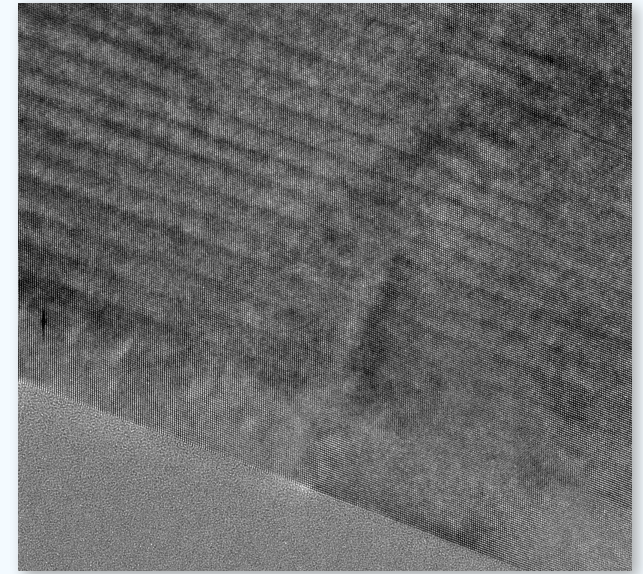
171×



50 nm

1.6 nm Ge + 12 nm Si

39×



10 nm

2 nm Ge + 1.5 nm Si

171×

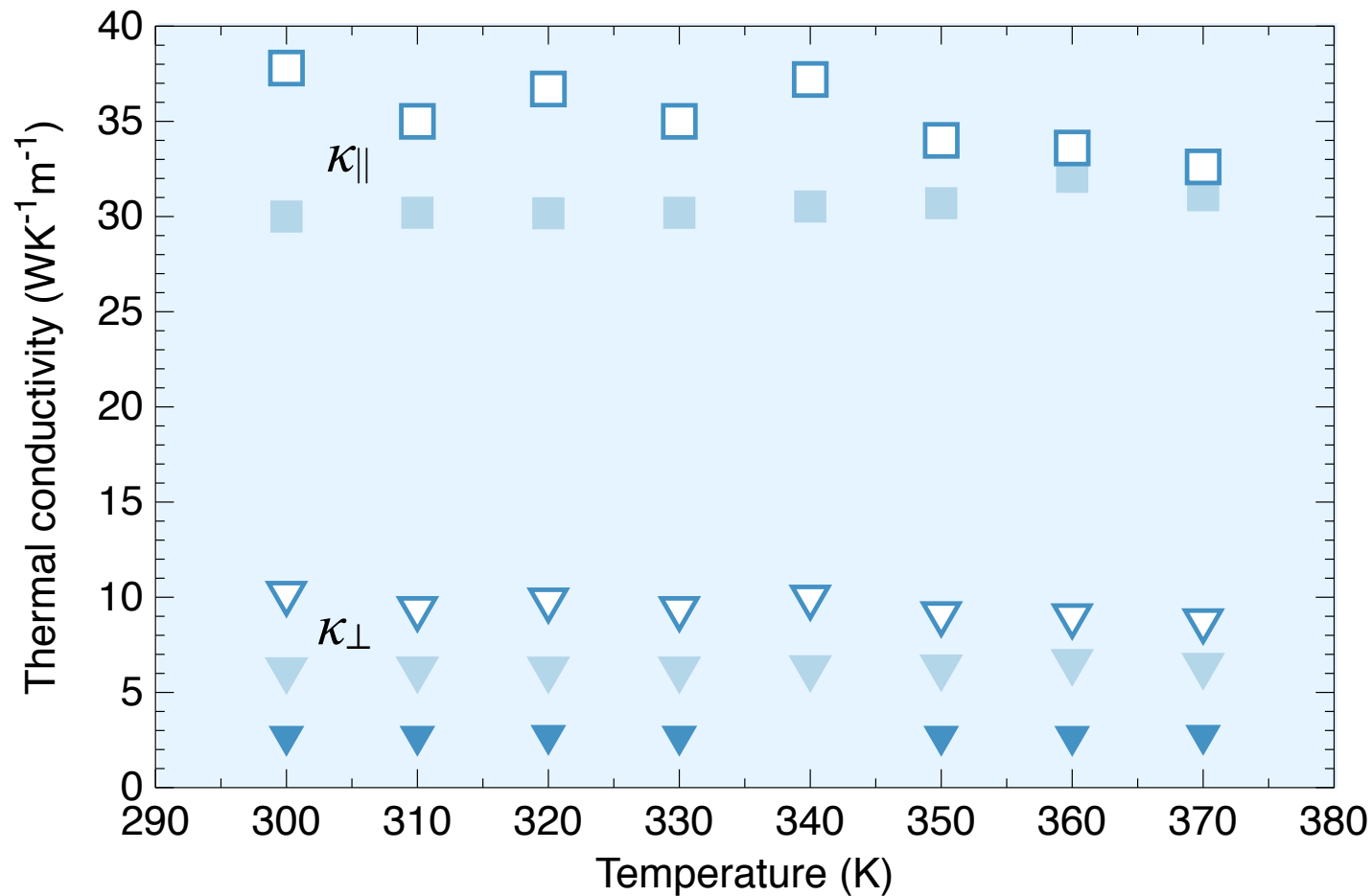
total Ge content of the film

1.7 %

3.5 %

17 %

Thermal conductivity of periodic SL



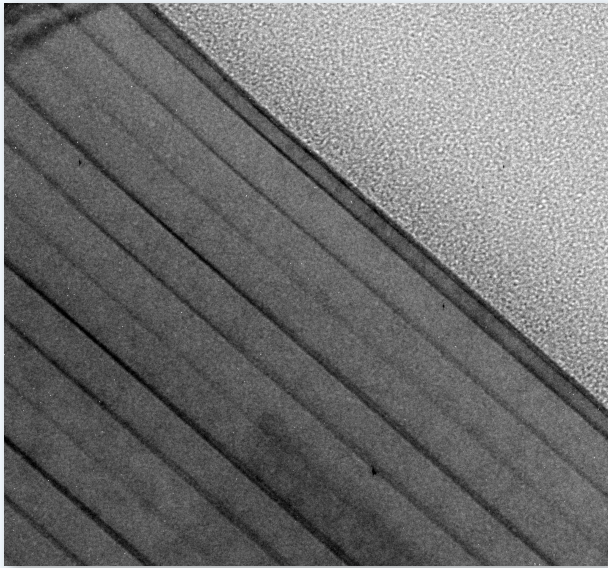
3.5 % Ge, period 13.6 nm
1.7 % Ge, period 4.5 nm

3.5 % Ge, period 13.6 nm
1.7 % Ge, period 4.5 nm
17 % Ge, period 4.5 nm

In-plane and cross-plane thermal conductivities for SLs
with different Ge contents and periods

Aperiodic multilayers

20 nm



1.2 nm Ge + 12 nm Si

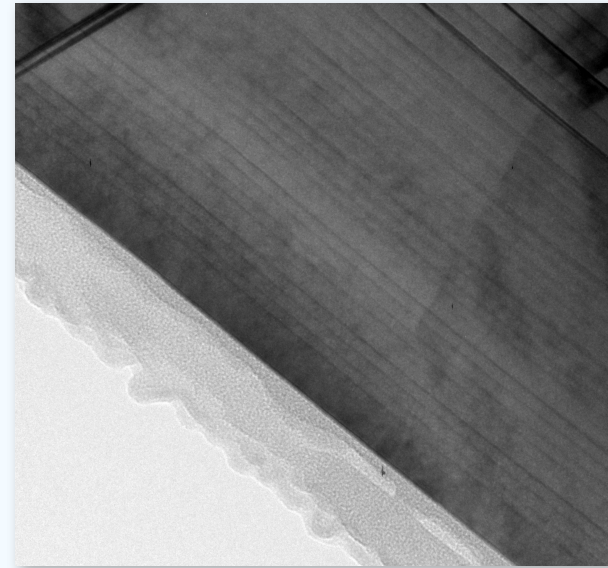
1.2 nm Ge + 12 nm Si

1.8 nm Ge + 12 nm Si

0.9 nm Ge + 12 nm Si

1.6 nm Ge + 12 nm Si

6×, ≈ 600 nm



0.6 nm Ge + 4.1 nm Si

0.3 nm Ge + 5.1 nm Si

0.8 nm Ge + 4.8 nm Si

0.6 nm Ge + 5.7 nm Si

0.6 nm Ge + 3.8 nm Si

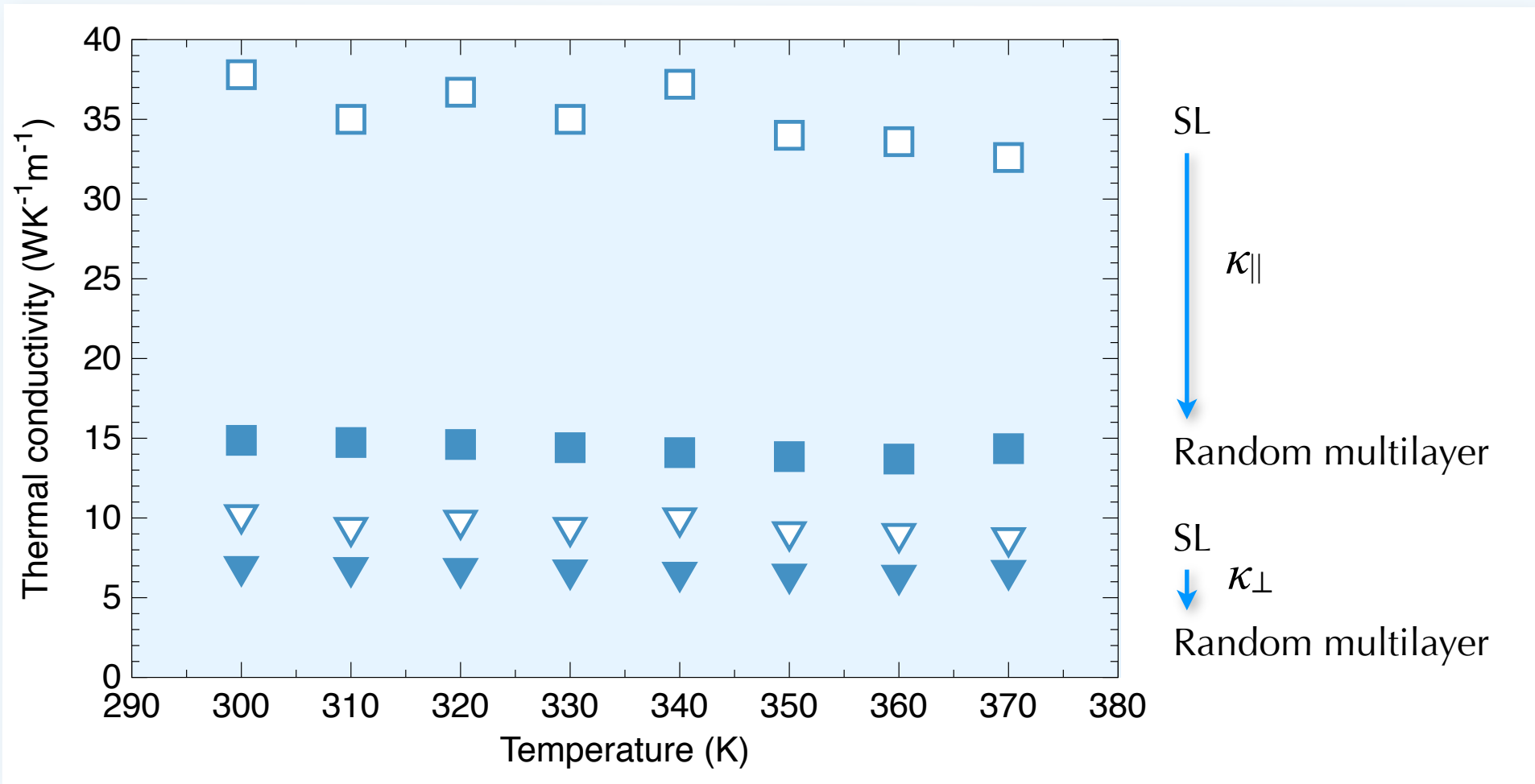
34×, ≈ 940 nm

Ge content

2.9 %

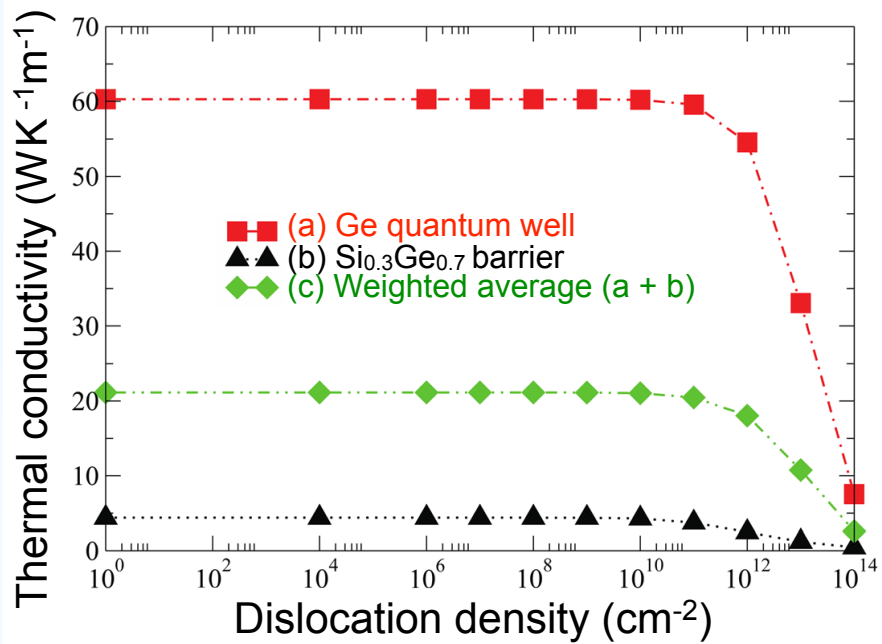
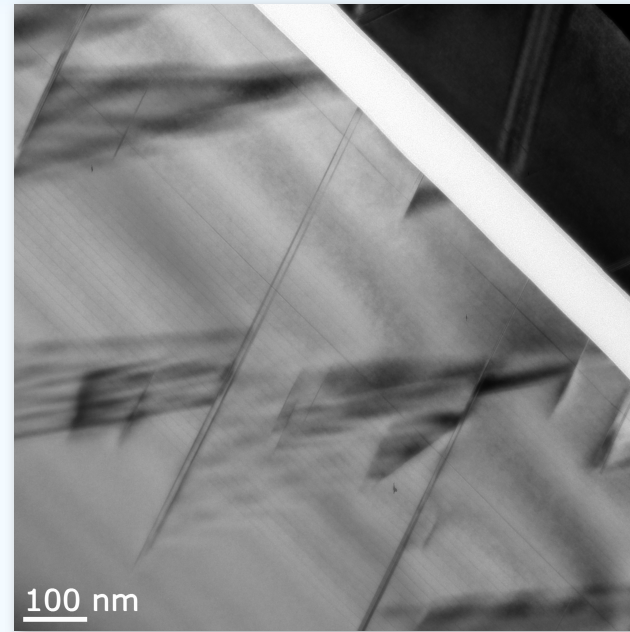
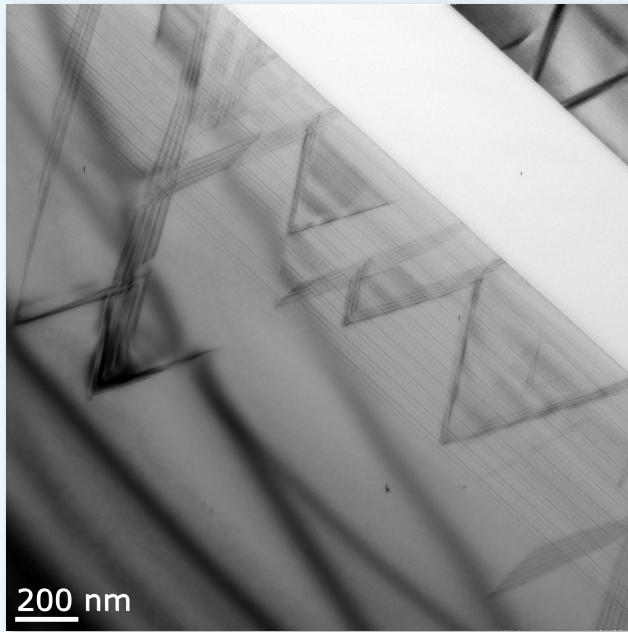
3.3 %

Results of random multilayers



Thermal conductivities in a random multilayer (2.9 % Ge)
in comparison to a superlattice(3.5 % Ge).

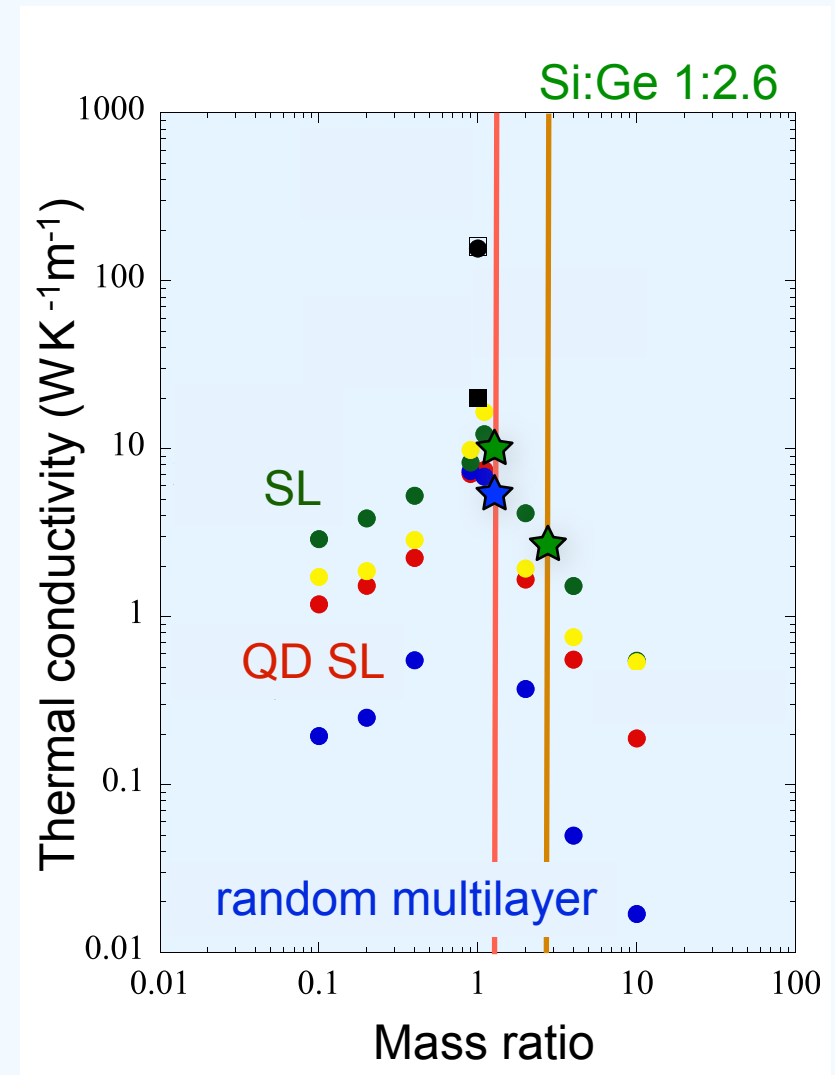
Defect issues



[Watling, Paul 2011]

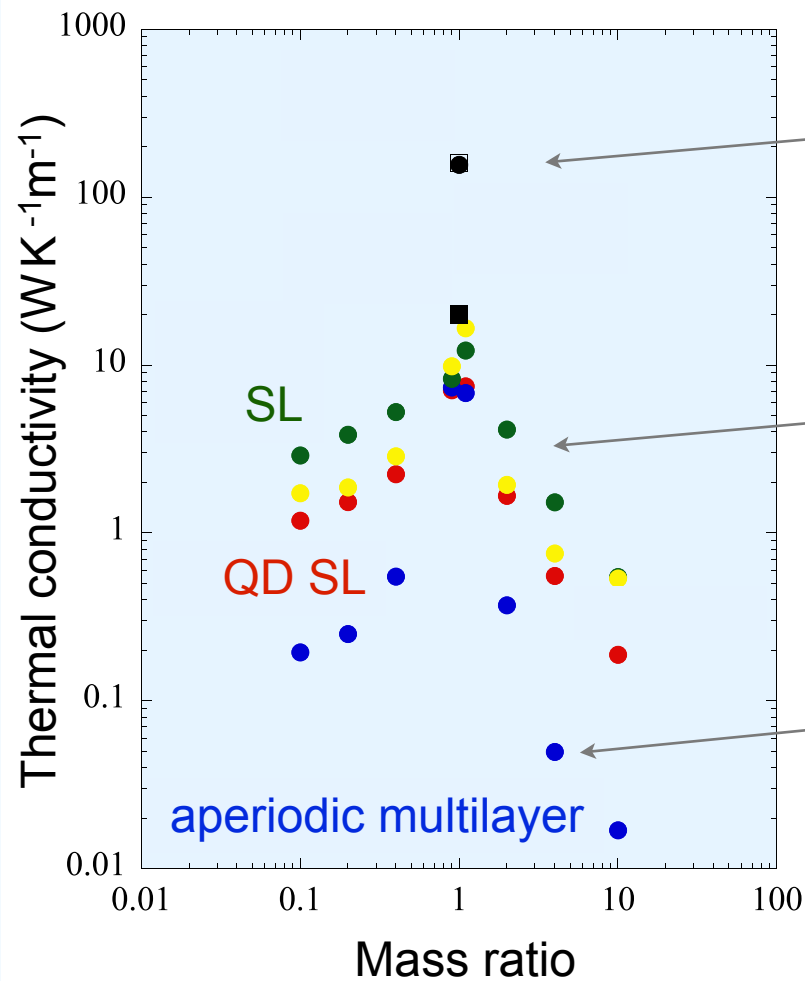
Comparison of thermal conductivities

- ◆ Lowest $\kappa_{\perp}$ for SL with highest Ge content
- ◆ $\kappa_{\perp}$ a function of the SL period
[cf. e.g. Rawat *et al*: J Appl Phys **105** (2009) 024909]
- ◆ Only a small reduction in $\kappa_{\perp}$ for random multilayers★ compared to SL★ observed due to low mass ratio in the multilayers investigated so far
- ◆ Random multilayers exhibit as well a decrease in $\kappa_{\parallel}$ by $\approx 50\%$



[Frachioni, White 2012]

Comparison of thermal conductivities



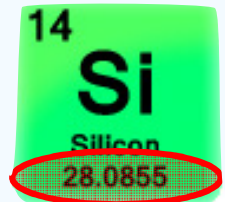
$$\kappa(\text{Si}) \approx 150 \text{ WK}^{-1} \text{ m}^{-1}$$

$$\kappa(\text{SiGe SL}) \approx 2 \text{ WK}^{-1} \text{ m}^{-1}$$

$$m_{\text{Ge}}/m_{\text{Si}} \approx 2.6$$

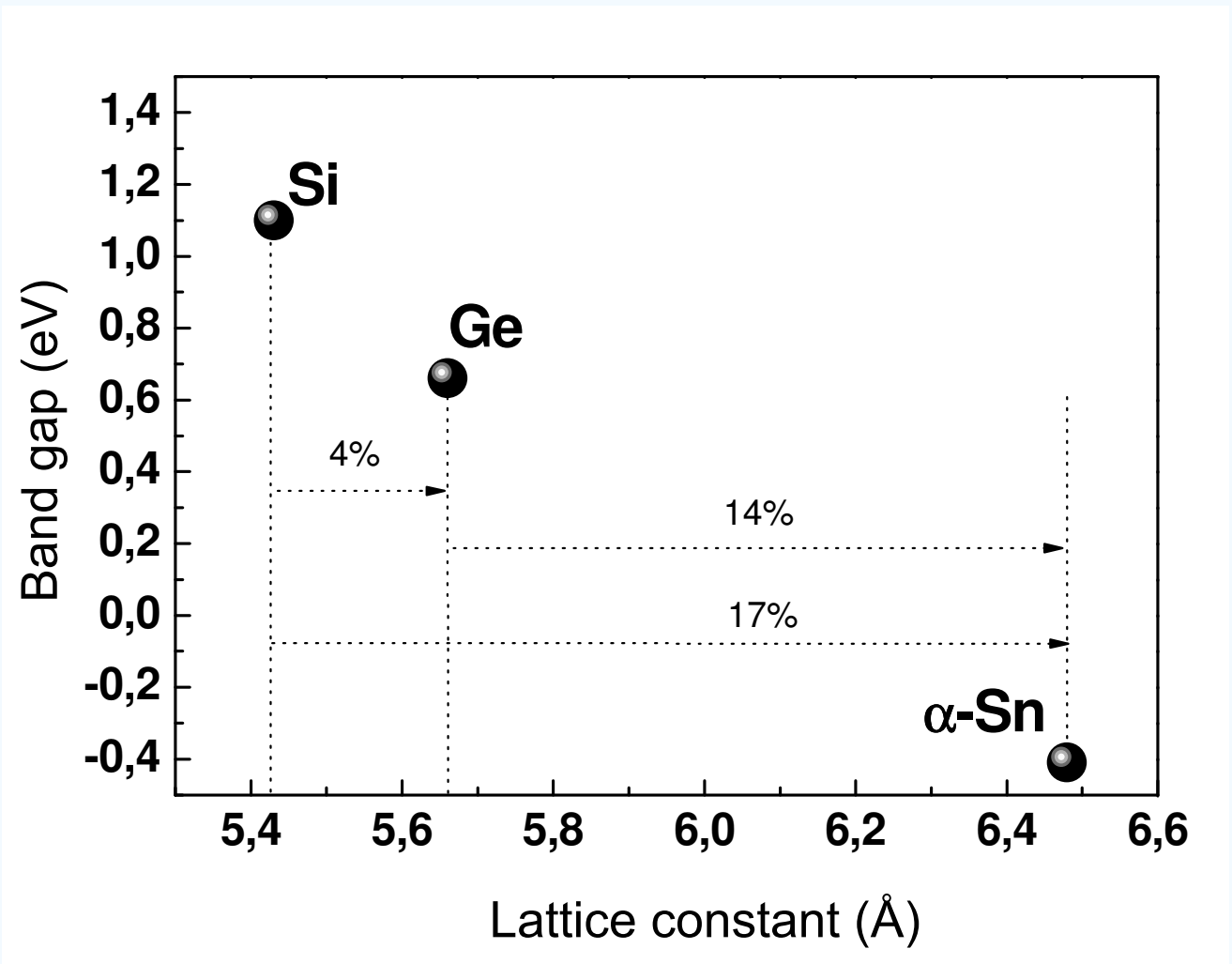
$$\kappa(\text{SiSn aML}) < 0.1 \text{ WK}^{-1} \text{ m}^{-1}$$

$$m_{\text{Sn}}/m_{\text{Si}} \approx 4.2$$



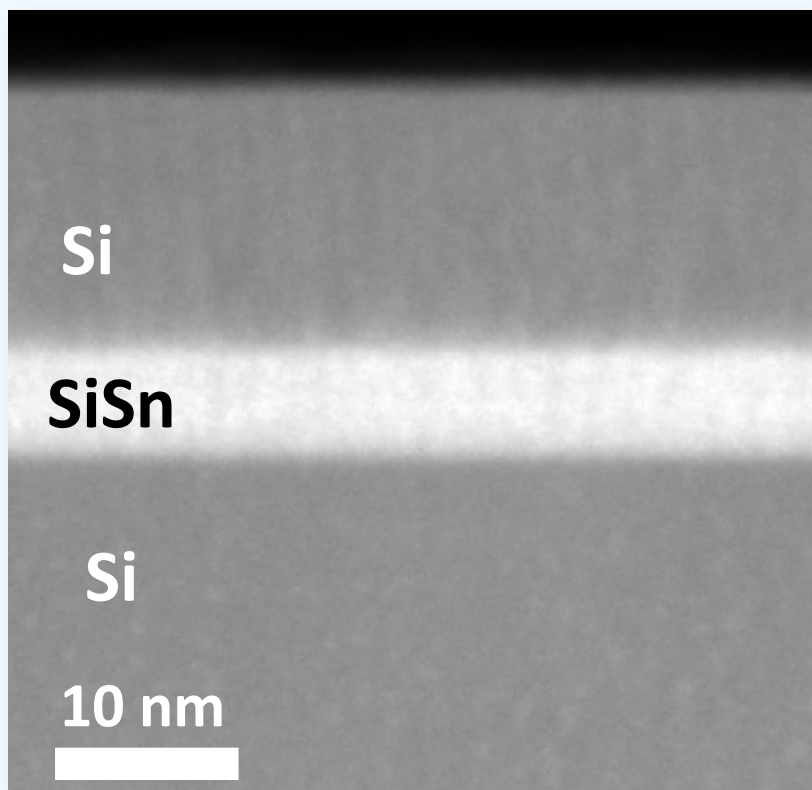
Si-Ge-Sn

IIIA 3A	IVA 4A	VA 5A
5 B Boron 10.811	6 C Carbon 12.011	7 N Nitrogen 14.00674
13 Al Aluminum 26.981539	14 Si Silicon 28.0855	15 P Phosphorus 30.973762
31 Ga Gallium 69.732	32 Ge Germanium 72.64	33 As Arsenic 74.92159
49 In Indium 114.818	50 Sn Tin 118.71	51 Sb Antimony 121.760
81 Tl Thallium 204.3833	82 Pb Lead 207.2	83 Bi Bismuth 208.98037
Basic Metal	Semimetals	Nonmetals

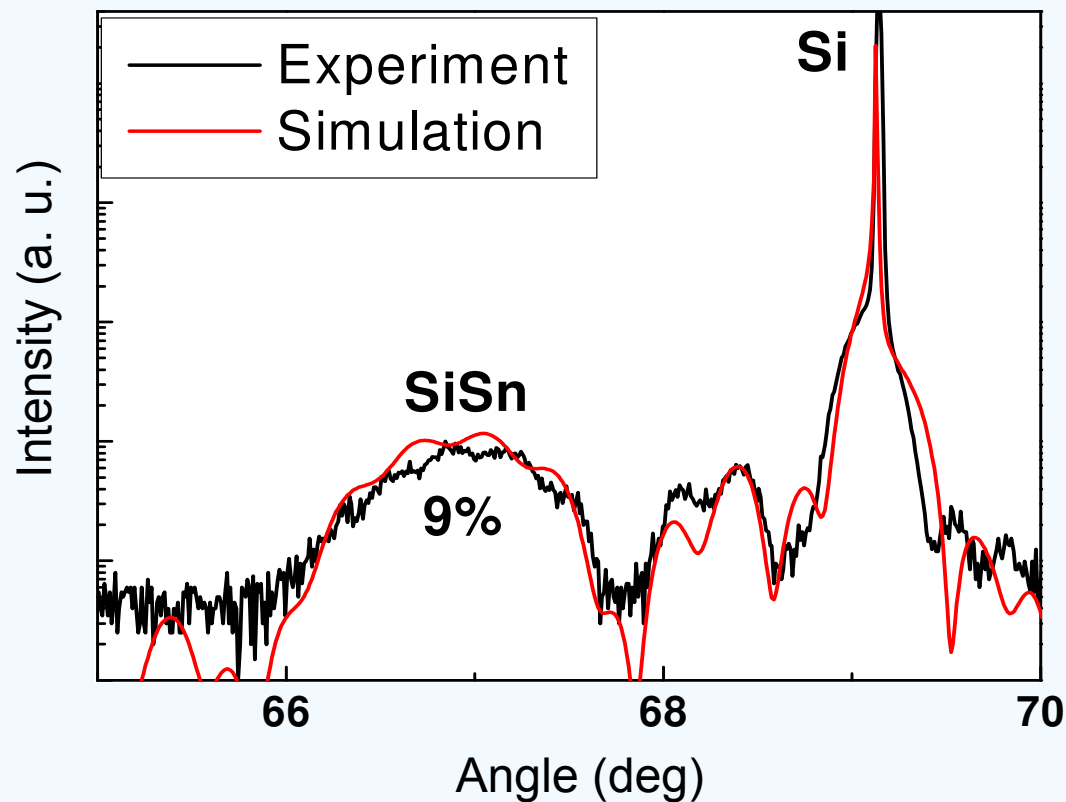


SiSn growth

MBE at 200 °C

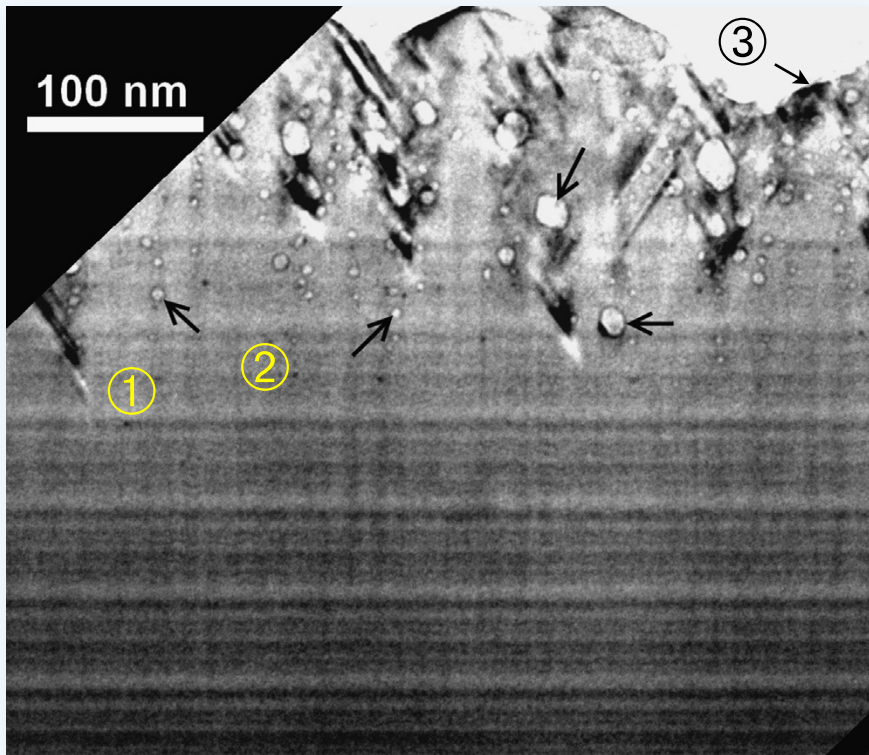


Cross-section TEM

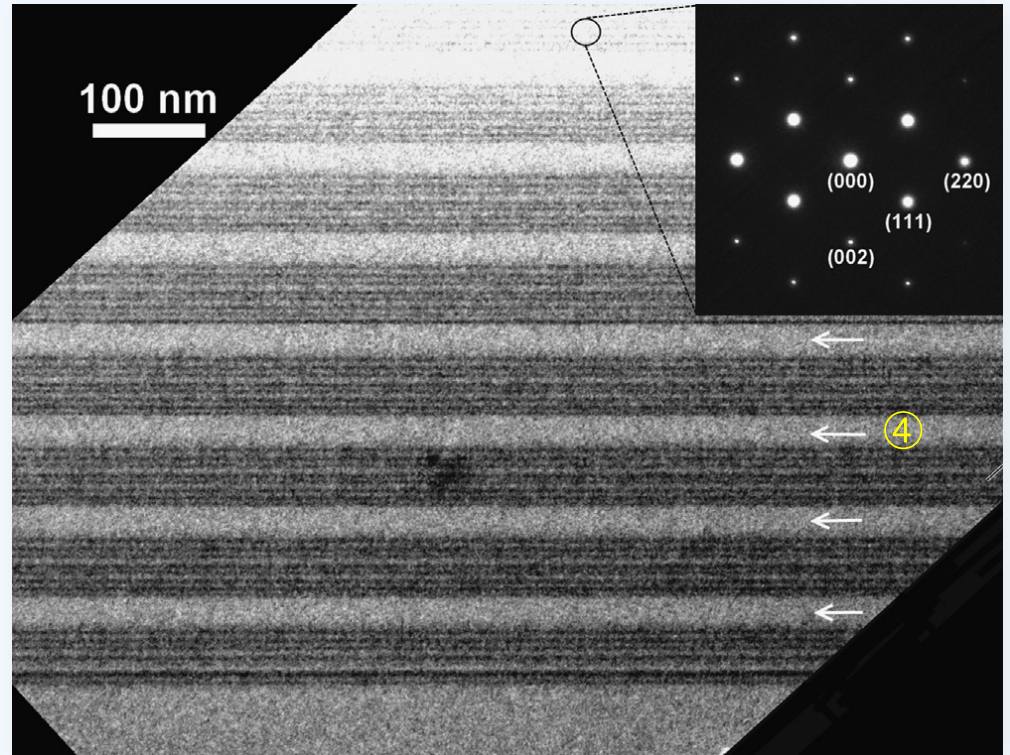


High-resolution X-ray Ω - 2θ scan

SiSn multilayers



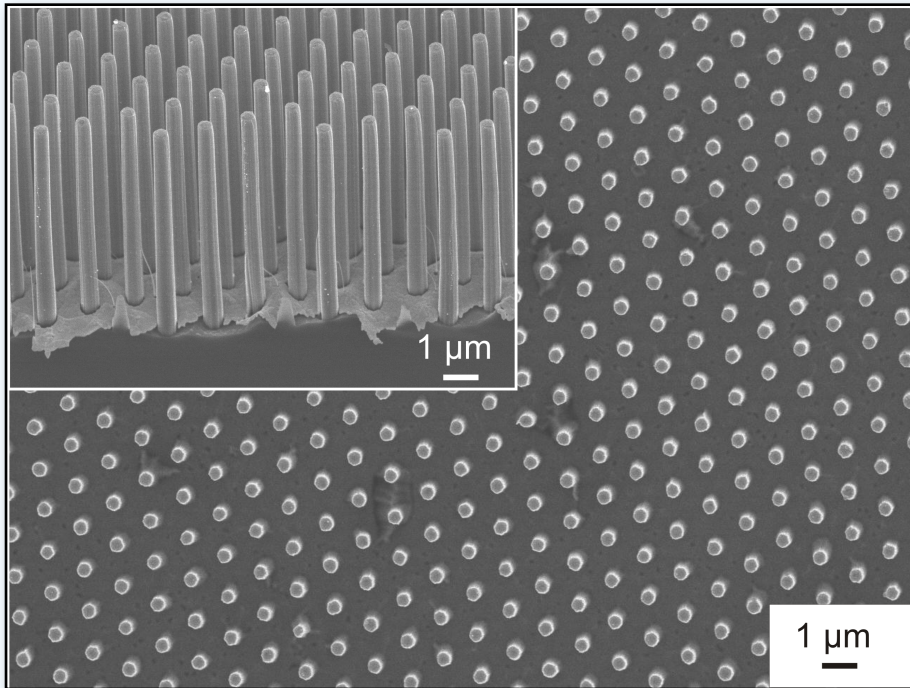
Defective growth with stacking faults
①, voids ②, amorphous Si/SiSn ③



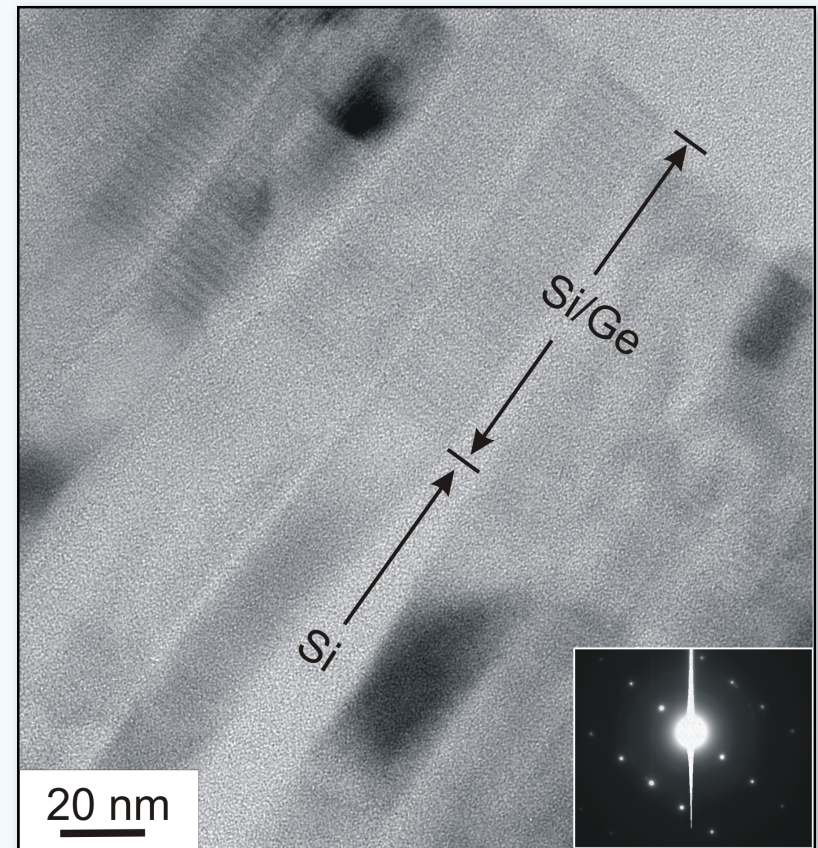
Defect-free multilayers with Si insertions
grown at high temperatures ④

Si-Ge nanowires

Geyer *et al* Nano Lett **9** (2009) 3106;
J Phys Chem **116** (2012) 13446



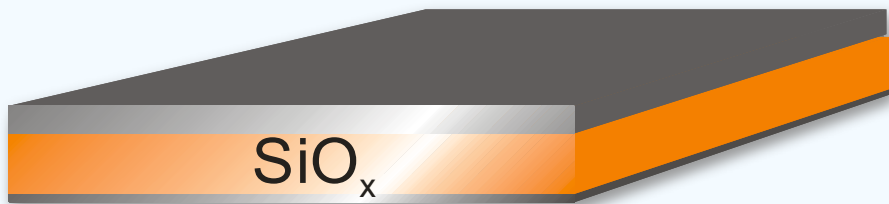
Silicon nanowires fabricated
by metal-assisted etching



Si-Ge superlattice
nanowires, $\varnothing$ below 20 nm

➡ Further reduction of the thermal conductivity
100× smaller than bulk Si for $\varnothing$ below 100 nm

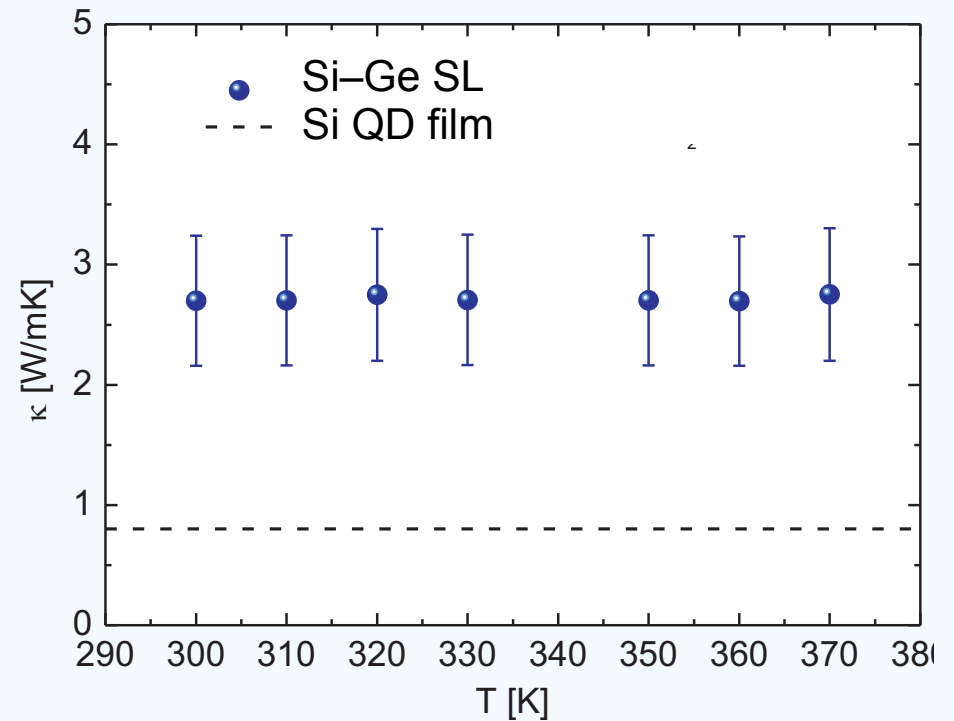
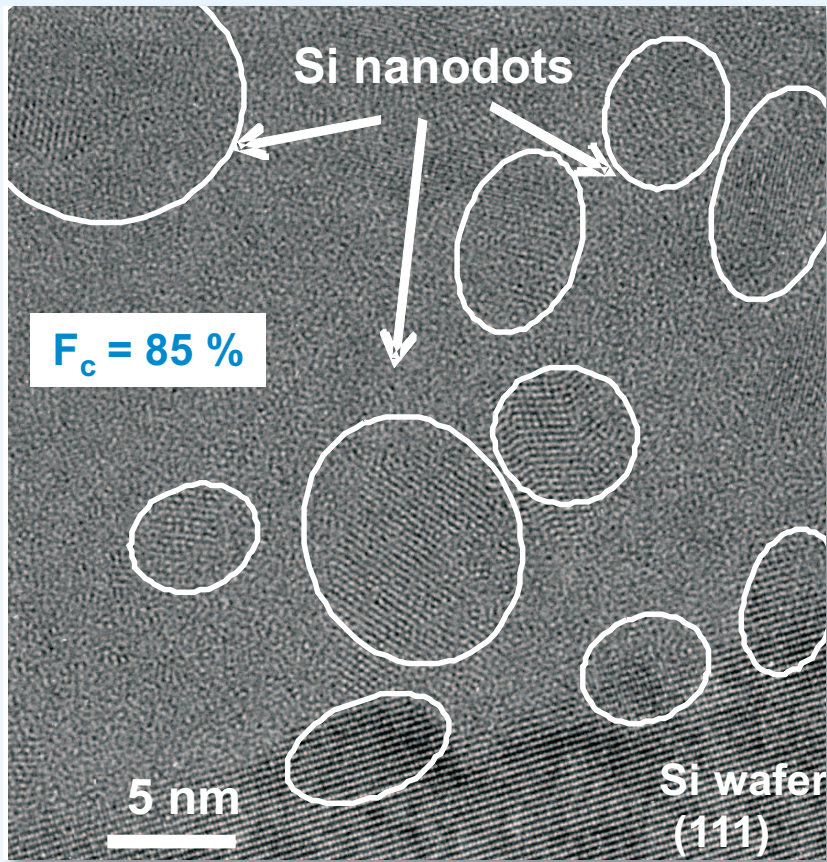
Si nanoparticles in Al_2O_3



Fabrication of nanoparticles in thin-film oxides

- ◆ Annealing $T > 560^\circ\text{C}$
- ◆ Conductivity $\sim 100 \text{ S/cm}$, Seebeck coefficient $S \approx 400 \mu\text{V/K}$

Composite films with Si quantum dots



High power factor can be achieved near to the percolation limit ($\sigma > 100$ S/cm, $S \approx 400$ μ V/K), thermal conductivity close to the oxide

Conclusions

- ➡ High-efficient thermoelectric thin-film devices based on Si and Ge possible
- ➡ Figure of merit for optimum structures higher than 2 for temperatures of RT ... 300 °C
- ➡ Diverse approaches such as Si–Ge SL, incorporation of defects, fabrication of nanopillars
- ➡ Technological feasibility: Epitaxy, PVD *etc.*

References

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- ♦ Roczen *et al* J Non-Cryst Sol (2011) 10.1016/j.jnoncrysol.2011.11.024.